

NISIN

内嵌式触摸液晶显示模组规格承认书

(IN-CELL) LCM Specifications for Approval

客户： 客户型号：			NS570HD3032AZ01		
批准 APPROVED	审核 CHECKED	拟制 DESIGNED	批准 APPROVED	审核 CHECKED	拟制 DESIGNED



修改记录

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2021-11-15	V00	初版发行	所有	

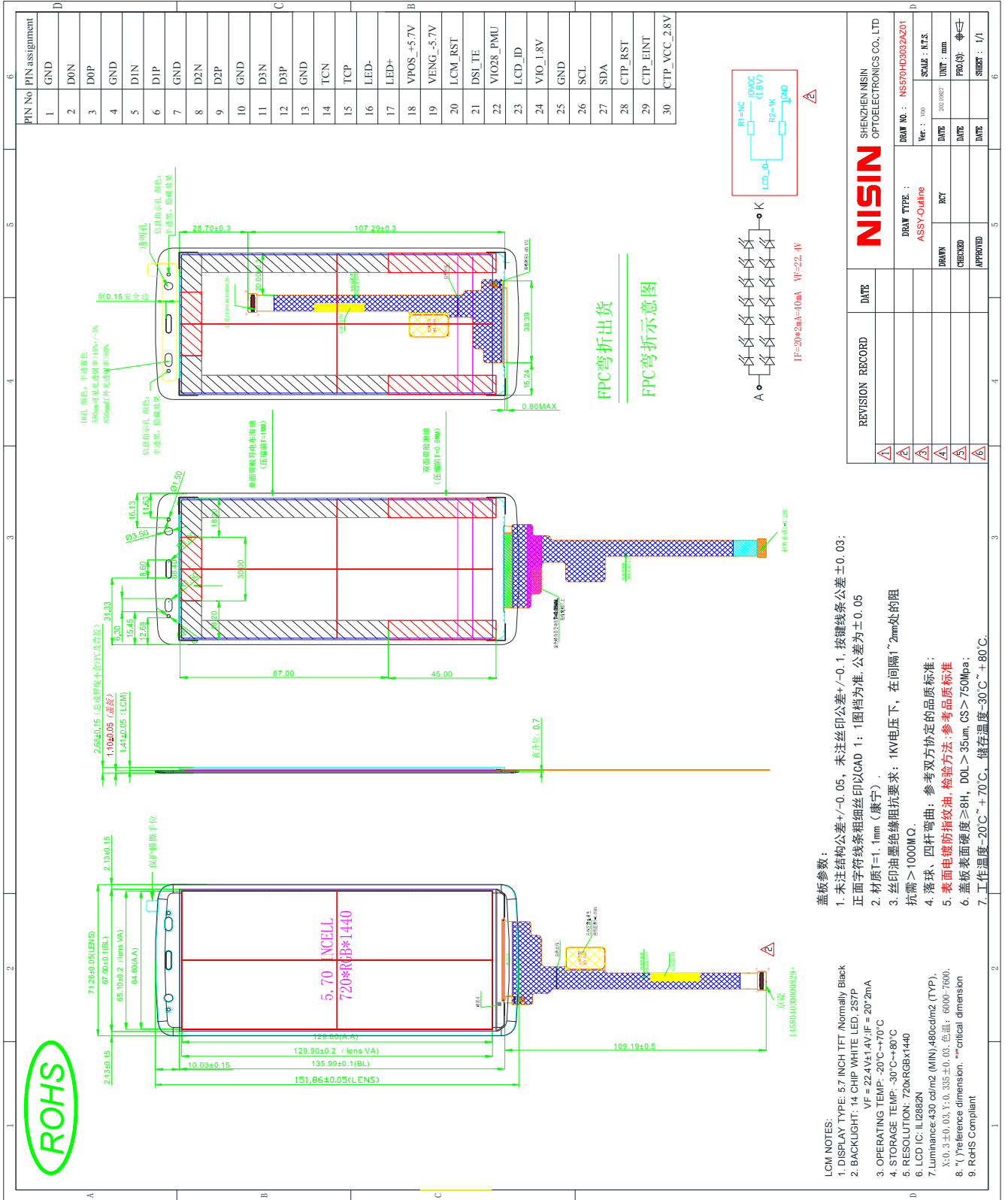
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1.产品规格 (Product Specifications)

面板类型 (Panel Type)	a-si TFT
面板尺寸 (Panel Size)	5.7 inch
显示类型 (Display Type)	Normal Black
分辨率 (Resolution)	720(RGB) x 1440 (dot)
显示点间距 (Dot Pitch)	0.03mm X 0.09mm
显示色彩 (color)	16.7M
视角 (View Angle)	ALL
显示驱动 IC (Display Driver IC)	ILI2882N
接口类型 (Interface Type)	MIPI 4 Lane
触摸类弄 (TP Type)	INCELL
触摸 IC (TP IC)	ILI2882N
触摸接口类型 (TP Interface)	I2C
外形尺寸 (Dimensions)	71.26(H) X 151.86(V) X 2.68(T) (mm)
显示区尺寸 (Display area)	64.8 x 129.6 (mm)
背光 (Back Light)	420 Cd/m ² (TYP)
触摸点数 Touch points	5
触摸按键 Touch Key Number	0

2. 产品图纸 (Product Drawings)



盖板参数:

1. 未注结构公差+/-0.05, 未注丝印公差+/-0.1, 按键线条公差±0.03; 正面对称线条粗细丝印以CAD 1:1 图档为准, 公差为±0.05
2. 材质T=1.1mm (磨宁)
3. 丝印油墨绝缘阻抗要求: 1KV电压下, 在间隔1.2mm处的阻抗>1000MO
4. 落球, 四杆弯曲: 参考双方协定的品质标准;
5. 表面电镀防锈纹油: 检验方法: 参考品质标准
6. 盖板表面硬度≥8H, DOI>35um, GS>750Mpa;
7. 工作温度-20°C~+70°C, 储存温度-30°C~+80°C.

LGM NOTES:

1. DISPLAY TYPE: 5.7 INCH TFT (Normally Black)
2. BACKLIGHT: 14 CHIP WHITE LED, D57P
VF = 22.4V@1.4V/IF = 20.2mA
3. OPERATING TEMP: -20°C~+70°C
4. STORAGE TEMP: -30°C~+180°C
5. RESOLUTION: 720*RGB*1440
6. LCD IC: IL12882N
7. Luminance 430 cd/m2 (MIN) 480cdm2 (TYP),
X:0.3±0.03, Y:0.335±0.03, 色温: 6000~7600.
8. Y: reference dimension. **critical dimension
9. RoHS Compliant

3.接口定义（The Interface Definition）

PIN No	PIN assignment				
1	GND	D	16	LED-	B
2	D0N		17	LED+	
3	D0P		18	VPOS_+5.7V	
4	GND		19	VENG_-5.7V	
5	D1N		20	LCM_RST	
6	D1P		21	DSI_TE	
7	GND		22	VIO28_PMU	
8	D2N	C	23	LCD_ID	
9	D2P		24	VIO_1.8V	
10	GND		25	GND	
11	D3N		26	SCL	
12	D3P		27	SDA	
13	GND		28	CTP_RST	
14	TCN		29	CTP_EINT	
15	TCP	30	CTP_VCC_2.8V		

4. 电性特性 (Electrical Characteristics)

14. Electrical Characteristics

14.1. Absolute Maximum Ratings

The absolute maximum rating is listed on the following table. When the ILI2882N is used out of the absolute maximum ratings, it may be permanently damaged. To use the ILI2882N within the following electrical characteristics limit is strongly recommended for normal operation. If these electrical characteristic conditions are exceeded during normal operation, the ILI2882N will malfunction and cause poor reliability.

Table 43. Absolute Maximum Ratings

Item	Symbol	Unit	Value
Supply voltage (I/O)	IOVCC ~ VSS	V	-0.3 ~ +1.95
Supply voltage (DSI I/O)	VDDAM ~ VSS	V	-0.3 ~ +1.95
Supply voltage	VSP ~ AVSS	V	-0.3 ~ +6.3
Supply voltage	VSN ~ AVSS	V	0.3 ~ -6.3
Driver Supply voltage	VGH ~ AVSS	V	-0.3 ~ +20
Driver Supply voltage	VGL ~ AVSS	V	0.3 ~ -18
Driver supply voltage	VSP - VSN	V	≤ 12V
Driver supply voltage	VGH - VGL	V	≤ 32V
Driver supply voltage	IOVCC - VCL	V	≤ 5.3V
Input voltage	V _{IN}	V	-0.3 ~ IOVCC + 0.3
HS Input voltage	V _{HSIN}	V	-0.3 ~ + 1.3
Operating temperature	T _{opr}	℃	-30 ~ +70
Storage temperature	T _{stg}	℃	-55 ~ +110

Note:

If one of the above parameters is exceeded the absolute maximum rating even momentarily, the quality of the product may be degraded. Absolute maximum ratings, therefore, specify the values exceeding which the product may be physically damaged. Be sure to use the product within the range of the absolute maximum ratings.

14.2. DC Characteristics for Panel Driving

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Power & Operation Voltage							
Logic operating voltage	IOVCC	-	1.65	1.8	1.95	V	
Operating Voltage	VSP	-	4.5	5.5	6.3	V	
Operating Voltage	VCI	-	2.5	2.8	3.3		
Operating Voltage	VSN	-	-8.3	-5.5	-4.5	V	
Logic operating voltage	VDDAM	-	1.65	1.8	1.95	V	
Logic High level input voltage	VIH	-	0.7*IOVCC	-	IOVCC	V	Note1
Logic Low level input voltage	VIL	-	0	-	0.3*IOVCC	V	Note1
Logic High level output voltage TE, LEDPWM	VoH	I _{OH} = -1.0mA	0.8*IOVCC	-	IOVCC	V	Note1
Logic Low level output voltage TE, LEDPWM	VoL	I _{oL} = +1.0mA	0	-	0.2*IOVCC	V	Note1
Driver Supply Voltage	-	VGH0-IVCH-VGL	-	-	34	V	
VCOM Operation							
DC VCOM Amplitude Voltage	VCOM	-	-3.0	-	0	V	Note2
Source Driver							
Positive Source Output Range	V _{SOUT}	-	0.2	-	GVDDP	V	Note3
Negative Source Output Range	V _{SOUT}	-	GVDDN	-	-0.2	V	Note3
Positive Gamma Reference Voltage	GVDDP	-	3	-	6	V	Note4
Negative Gamma Reference Voltage	GVDDN	-	-6	-	-3	V	Note4
Source Output Setting Time	Tr	Below with 99% precision	-	3.5		us	Note2,3
Source Output Deviation Voltage	VDEV	S _{out} >=4.2V S _{out} <=0.8V	-	40		mV	Note2
		4.2V>S _{out} >0.8V	-	30		mV	-
Source Output Offset Voltage	V _{OFFSET}	-	-	50		mV	Note2
Booster Operation							
Gate Driver High Voltage	VGH	-	8	-	20	V	
Gate Driver Low Voltage	VGL	-	-6	-	-18	V	

Note:

1. Ta = -30 to 70 °C (to 85 °C no damage), IOVCC=1.65V ~ 1.95V.
2. Source Channel Loading = 6.5Kohm, 50pF/Channel
3. The Max. Value is between with Note-2 measure point and Gamma setting value
4. GVDDP ≤ VSP-0.3V and GVDDN ≥ VSN+0.3V.

14.3.1. DSI Timing Characteristics

14.3.1.1. High Speed Mode – Clock Channel Timing

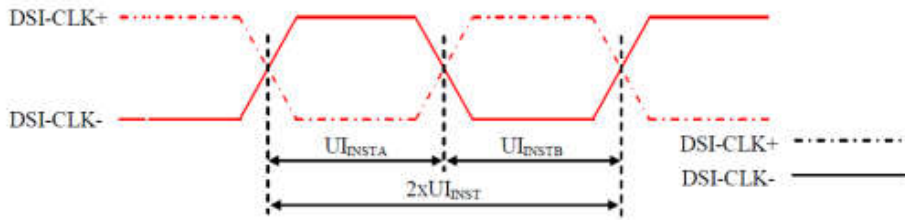


Figure 102. DSI Clock Channel Timing

Table 45. DSI Clock Channel Timing

Signal	Symbol	Parameter	Min.	Max.	Unit
DSI-CLK+/-	$2xUI_{INST}$	Double UI instantaneous	2	25	ns
DSI-CLK+/-	UI_{INSTA}, UI_{INSTB}	UI instantaneous Half	1	12.5	ns

Note: $UI_{INST} = UI_{INSTA} = UI_{INSTB}$

14.3.1.2. High Speed Mode – Data Clock Channel Timing

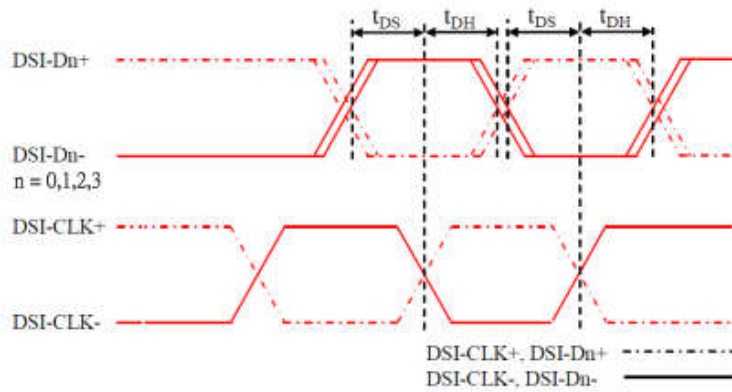


Figure 103. DSI Data to Clock Channel Timings

14.3.1.3. High Speed Mode – Rise and Fall Timings

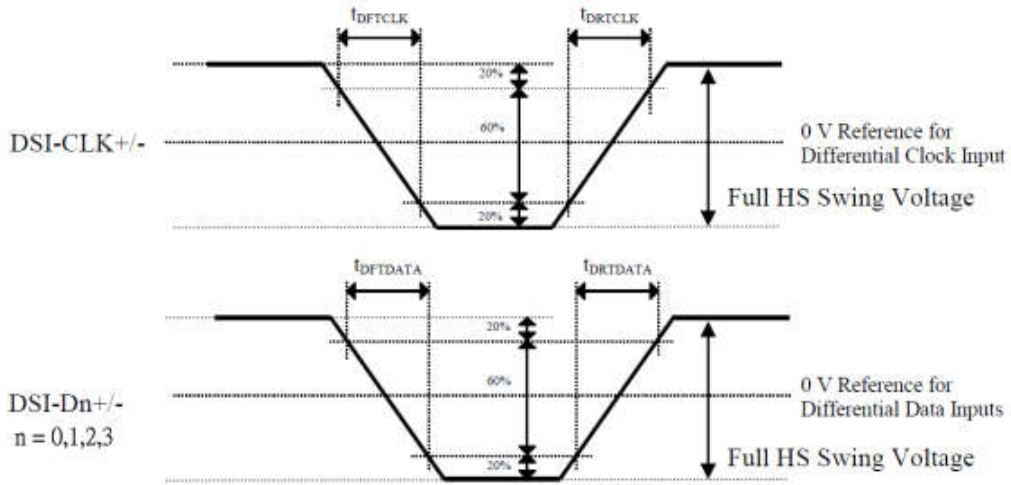


Figure 104. Rise and Fall Timings on Clock and Data Channels

Table 47. Rise and Fall Timings on Clock and Data Channels

Parameter	Symbol	Condition	Specification		
			Min.	Typ.	Max.
Differential Rise Time for Clock	t _{DRTCLK}	DSI-CLK+/-	150 ps	-	0.3UI
Differential Rise Time for Data	t _{DRTDATA}	DSI-Dn+/- (n=0,1,2,3)	150 ps	-	0.3UI
Differential Fall Time for Clock	t _{DFTCLK}	DSI-CLK+/-	150 ps	-	0.3UI
Differential Fall Time for Data	t _{DFTDATA}	DSI-Dn+/- (n=0,1,2,3)	150 ps	-	0.3UI

Note:

The display module has to meet timing requirements, what are defined for the transmitter (MCU) on MIPI D-Phy standard.

14.3.1.4. Low Speed Mode – Bus Turn Around

Lower Power Mode and its State Periods are illustrated for reference purposes on the Bus Turnaround (BTA) from the MCU to the Display Module (ILI2882N) sequence below.

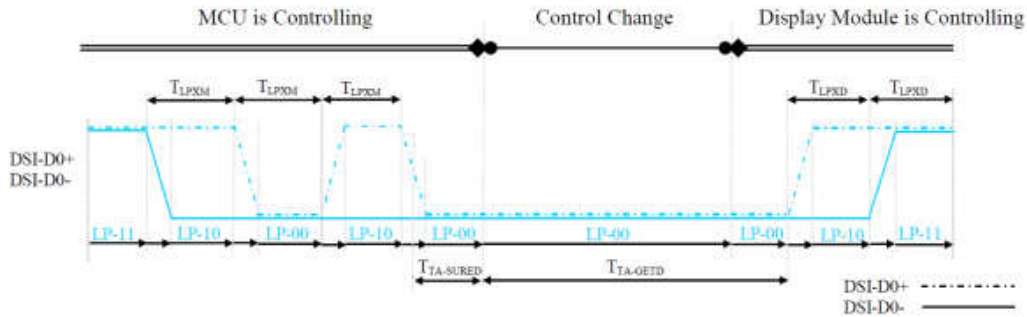


Figure 105. BTA from the MCU to the Display Module

Lower Power Mode and its State Periods are illustrated for reference purposes on the Bus Turnaround (BTA) from the Display Module (ILI2882N) to the MCU sequence below.

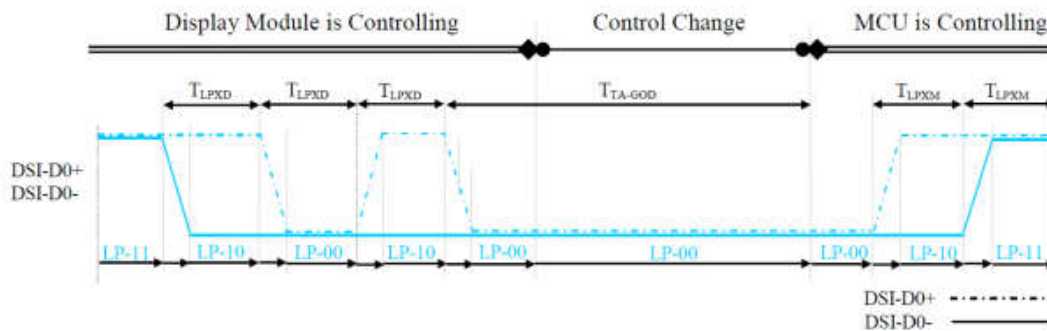


Figure 106. BTA from the Display Module to the MCU

Table 48. Low Power State Period Timings – A

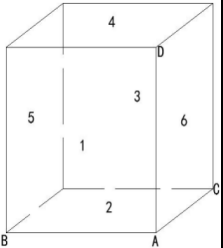
Table 48. Low Power State Period Timings – A

Signal	Symbol	Description	Min	Max	Unit
DSI-D0+/-	T_{LPXM}	Length of LP-00, LP-01, LP-10 or LP-11 periods MCU → Display Module (ILI2882N)	50	75	ns
DSI-D0+/-	T_{LPXD}	Length of LP-00, LP-01, LP-10 or LP-11 periods Display Module (ILI2882N) → MCU	50	75	ns
DSI-D0+/-	$T_{TA-SURED}$	Time-out before the Display Module (ILI2882N) starts driving	T_{LPXD}	$2 * T_{LPXD}$	ns

Table 49. Low Power State Period Timings – B

Signal	Symbol	Description	Time	Unit
DSI-D0+/-	$T_{TA-GETD}$	Time to drive LP-00 by Display Module (ILI2882N)	$5 * T_{LPXD}$	ns
DSI-D0+/-	T_{TA-GOD}	Time to drive LP-00 after turnaround request – MCU	$4 * T_{LPXD}$	ns

5.可靠性实验测试(Reliability Test Conditions And Methods)

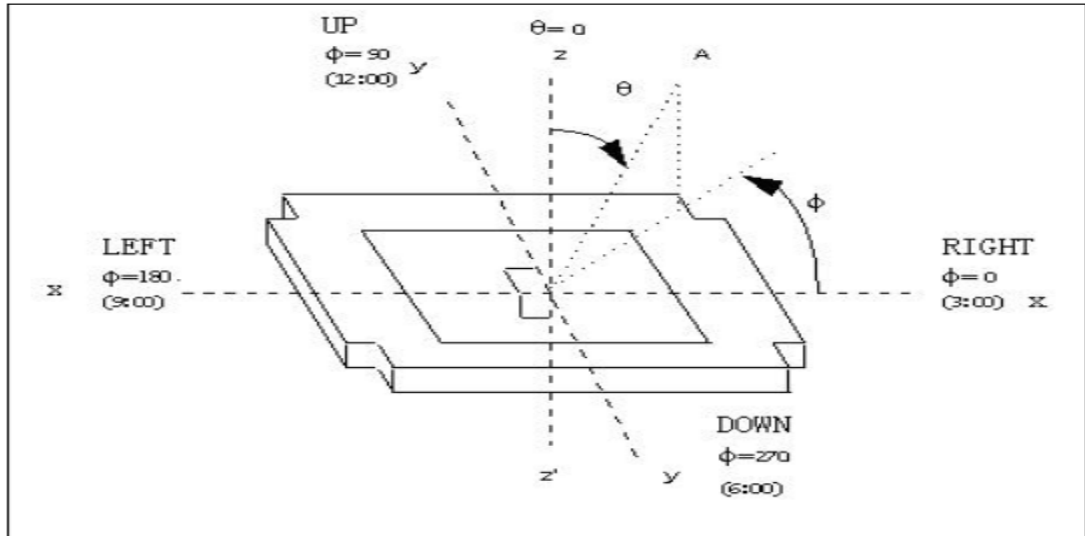
序号	试验项目	试验条件及方法	试验设备	检验项目	检验工具												
1	高温高湿(静、动态)试验	温度 60℃±3℃, 湿度 90%±3%, 要求选择时间分别为 96 小时, 静、动态(产品点亮)在室温下恢复 2 小时后进行外观, 显示功能检查。	恒温恒湿试验机	检验外观、功能、抗腐蚀性	目视/测试架/客户样机/显微镜												
2	高、低温冲击试验	静态-30℃(30分钟)↔80℃(30分钟)↔-30℃(30分钟), 24个循环, 在室温下恢复2小时后进行外观, 显示功能检查。	冷热冲击试验机	检验外观、功能													
3	高温贮存试验	常温60℃+/-3℃、宽温70℃+/-3℃、96小时后在室温状态下恢复1小时在2小时内完成外观、显示功能检查。	烤箱	检验外观、功能	目视/测试架/客户样机												
4	低温贮存试验	常温-20℃+/-3℃、宽温-30℃+/-3℃、条件的试验箱内保存96小时后在室温状态下恢复1小时, 在2小时完成外观、显示功能检查, 特别注意检查是否有漏液、断线、腐蚀、偏光片不良现象。	低温冰箱	检验外观、功能													
5	低温贮存试验(动态)	常温-20℃+/-3℃、宽温-30℃+/-3℃条件的试验箱内点亮刷屏, 过程中每1小时观察一次, 检查显示功能, 如: 异常, 卡机, 花屏等。特别注意检查是否有漏液、断线、腐蚀、偏光片不良现象。	低温冰箱	检验外观、功能	目视/测试架/客户样机												
6	包装模组跌落试验	<p>1、跌落重量及自由落体高度: (图二)</p>  <p>2、自由落体角度如下:</p> <table border="1" data-bbox="284 1545 662 1904"> <thead> <tr> <th>总重量</th> <th>自由落体高度</th> </tr> </thead> <tbody> <tr> <td>0-9kg</td> <td>92cm</td> </tr> <tr> <td>9-25kg</td> <td>76cm</td> </tr> <tr> <td>25-45kg</td> <td>53cm</td> </tr> <tr> <td>45-68kg</td> <td>46cm</td> </tr> <tr> <td>大于 68kg</td> <td>41cm</td> </tr> </tbody> </table> <p>1) 一角: A角 2) 三菱: A-B, A-D, A-C 3) 六面: 面1, 面2, 面3, 面4, 面5, 面6;</p>	总重量	自由落体高度	0-9kg	92cm	9-25kg	76cm	25-45kg	53cm	45-68kg	46cm	大于 68kg	41cm	包装模组跌落架	测试电性能无异常、外观检验无破损, 无脱离现象	目视/测试架/客户样机
总重量	自由落体高度																
0-9kg	92cm																
9-25kg	76cm																
25-45kg	53cm																
45-68kg	46cm																
大于 68kg	41cm																

7	盐雾试验	标准条件:中性盐雾试验(NSS试验):5%的氯化钠盐水溶液,溶液PH值中性(6.5~7.2),试验温度 $35\pm 2^{\circ}\text{C}$,盐雾的沉降率在 $1\sim 2\text{ml}/80\text{cm}^2\cdot\text{h}$ 之间,时间24h。2.其它特殊要求条件:醋酸盐雾试验(ASS试验):5%氯化钠溶液中配入冰醋酸,溶液PH值为3左右,试验温度 $35\pm 2^{\circ}\text{C}$,盐雾的沉降率在 $1\sim 2\text{ml}/80\text{cm}^2\cdot\text{h}$ 之间,时间24h。	盐雾试验设备	检验外观、功能,盐雾试验结果的判定方法,腐蚀物出现判定法:定性判定,试验后功能测试应OK,外观观察产品无腐蚀现象产生。	目视/测试架/客户样机/显微镜
8	ESD 抗静电试验	测试架测试状态下试验:接触4KV,非接触(空气)8KV放电测试	抗静电枪 (尖头接触放电,圆头空气放电)	检验外观、功能	目视/测试架

6. 光电参数 (Optical Characteristics)

6. OPTICAL SPECIFICATION

Item	Symbol	Conditions	Specifications			Unit	Note	
			Min.	Typ.	Max.			
Transmittance (w/o APCF)	T%	Viewing normal angle $\theta_x = \theta_y = 0^\circ$	2.92	3.44		%	All left side data are based on Innolux's following condition – 1.LC: AAS 2.Light Source: INX's BLU 3. Polarizer : Front: GRT1794XHUHC3 Rear: APCF04SD5MSCA124E 4.Measurement machine : DMS-900 5. Transmittance w/o APCF 6. VLC dark < 0.4V, VLC white > 5.4V	
Contrast Ratio	CR		1200	1500		--		
Response Time	$T_{on} + T_{off}$			30	35	ms		
Viewing Angle	Hor.	θ_{x+}	75	80		deg.		
		θ_{x-}	75	80				
	Ver.	θ_{y+}	75	80				
		θ_{y-}	75	80				
CF only Color Chromaticity (CIE 1931)	Red	Rx	0.641	0.661	0.681	-		Under C light Simulation
		Ry	0.305	0.325	0.345	-		
	Green	Gx	0.257	0.277	0.297	-		
		Gy	0.550	0.570	0.590	-		
	Blue	Bx	0.119	0.139	0.159	-		
		By	0.068	0.088	0.108	-		
	White	Wx	0.282	0.302	0.322	-		
		Wy	0.309	0.329	0.349	-		
	Color Gamut		65	69.2		%		



7.检验标准 (Inspection standard)

8.1 Inspection conditions is as follows

- 1) Viewing angle is within $\pm 30^\circ$ from vertical direction, as fig 1
- 2) Viewing angle is the angle defined in the drawing
- 3) Ambient temperature is approximately $25 \pm 5^\circ \text{C}$
- 4) Ambient luminance is about 300~500 Lux.

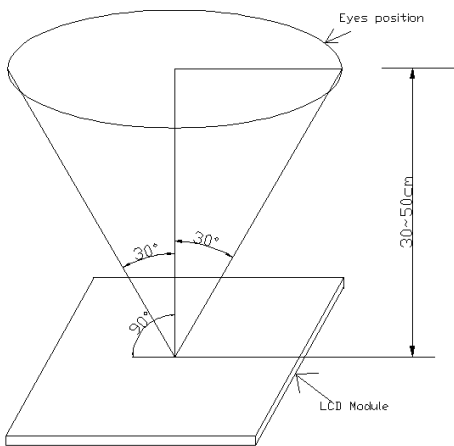
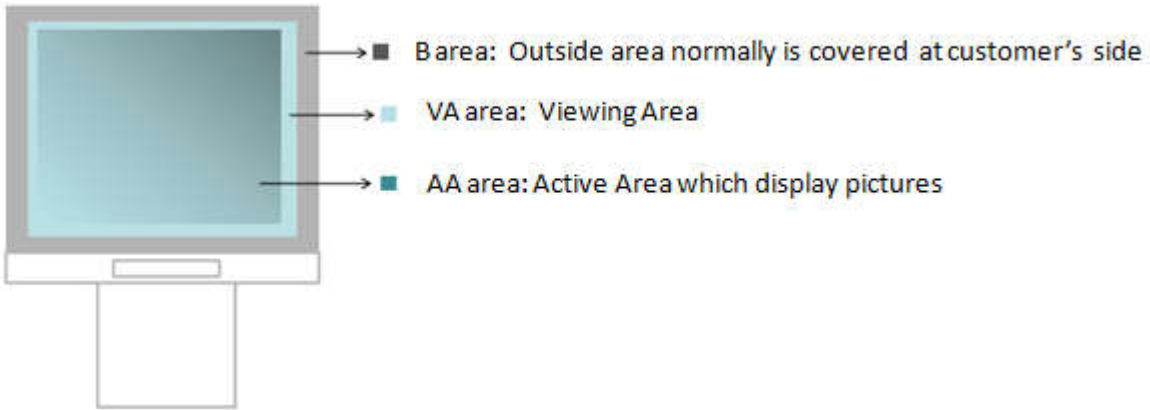


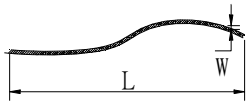
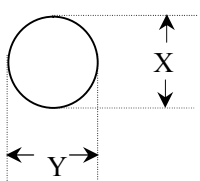
fig1

8.2 Panel area definition



8.3 Routine inspection standards

项目	不良定义	不良现象	判定标准		检验方法	
11. 3. 1	外观尺寸	与图纸尺寸不相符	NG		卡尺	
11. 3. 2	功能	显示少线	NG		目视	
		无显示	NG		目视	
		显示异常	NG		目视	主
		TP 功能不良, 无触摸	NG		目视/用手触摸	主
11. 3. 3	点亮产品可见及在LCD或T/P上有擦拭不掉的点状物	偏光片刺伤、脏点、圆形物、黑点 $\Phi = (L+W)/2$	LCM/总成 0.95 寸—2.4 寸 $\Phi \leq 0.10mm$	1、距产品30mm 目视不见忽略。 2、5mm 间距内只允许3个点。 3、显示区只允许10个点, 超过以上第2第3项则NG。	目视 (用菲淋卡比对)	次

			$0.10\text{mm} < \Phi \leq 0.15\text{mm}$ $\Phi > 0.15\text{mm}$	1 NG			
			0.15mm < Φ \leq 0.2mm 按照 A-品入库				
			LCM/总成 > 2.4 寸——6.0 寸				
			$\Phi \leq 0.10\text{mm}$	1、10mm 间距内只允许 3 个 2、显示区只允许 10 个点，超过以上任意一项则 NG	目视 (用菲淋卡比对)		
			$0.1\text{mm} < \Phi \leq 0.15\text{mm}$	4 (TP、屏各允许 2 个)			
			$0.15\text{mm} < \Phi \leq 0.2\text{mm}$	2 (TP、屏各允许 1 个)			
			$\Phi > 0.2\text{mm}$	NG			
11.3.4	点亮产品可见及在 LCD 或 T/P 上有擦拭不掉的线状物/刮伤		LCM/总成 0.95 寸——6.0 寸			目视 (用菲淋卡比对)	次
			长(L)	宽(W)	允许个数		
			$\leq 1\text{mm}$	$\leq 0.03\text{mm}$	2		
			$\leq 2\text{mm}$	$0.03 < W \leq 0.05\text{mm}$	1		
			$> 2\text{mm}$	$> 0.05\text{mm}$	NG		
			两条线毛之间必须距离 5mm 以上 (0.95 寸—3.0 寸). 两条线毛之间必须距离 10mm 以上 (3.1 寸—6.0 寸).				
11.3.5	偏光片气泡	$\Phi = (X+Y) / 2$ 	尺寸	允许个数	在日光台灯下撕起保护膜, 距待测物 30cm 目视	次	
			1、 $\Phi \leq 0.1\text{mm}$ 2、不超过边框 1/3	不计 (密集不可)			
			$0.10 < \Phi \leq 0.2\text{mm}$	1			
			$\Phi > 0.2\text{mm}$	NG			
			$0.2 < \Phi \leq 1.5\text{mm}$, (边框以外)	3			
			0.95 寸-2.4 寸气泡间距大于 5mm 以上 > 2.4 寸-6.0 寸气泡间距大于 10mm 以				

			上		
11.3.6	T/P 及偏光片 凹凸点	T/P: LCD 偏光片上有凹 凸点	可视区有水纹(擦拭不掉)拒 收 未进入可视区允收,客户装机 后不见允收	在同一 视角下 用样品 比对	次
11.3.7	<u>Mura</u>	边框四周或任一侧的色 差、较画面深、区域云状 不均、固定位置之图形凹 陷状、封口部分较画面深 的半圆形、一圈圈均匀的 色差、线状 mura、黑画面 可见因 spacer 聚集产生的 mura、均匀的实斜线、区 域性斜线、Driver IC 与 TFT 匹配问题等原因的 mura	1.判定示画面为 128 灰阶画面, 用 ND filter 盖住 mura 位置进行 判定。 2、ND1.3 (ND5%可遮盖不见) 3、双方若有签 限度样品,优先 限度样品。	ND filter, 128 灰阶 画面	次